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Marc Daumas, Claire Finot

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***Algorithm, Proof and Performances of  
a new Division of Floating Point Expansions***

Marc Daumas, CNRS

Claire Finot, ENS de Lyon

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\_\_\_\_\_ THÈME 2 \_\_\_\_\_

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# Algorithm, Proof and Performances of a new Division of Floating Point Expansions

Marc Daumas, CNRS  
Claire Finot, ENS de Lyon

Thème 2 — Génie logiciel  
et calcul symbolique  
Projet Arénaire

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**Abstract:** We present in this work a new algorithm for the division of floating point expansions. Floating expansion is a multiple precision data type developed with arithmetic operators that use the processor floating point unit for core computations instead of the integer unit. Researches on this subject have arisen recently from the observation that the floating point unit becomes a more and more efficient part of modern computers. Many simple arithmetic operators and some very useful geometric operators have already been presented on expansions. Yet previous work presented only a very simple division algorithm. We present in this work a new algorithm. We take this opportunity to extend the set of geometric operators with Bareiss' determinant on a matrix of size between 3 and 10. Running times with different determinant algorithms on different machines are compared with other multiprecision packages including GMP, CADNA and a computer geometry package working with modular arithmetic.

**Key-words:** Exact arithmetic, multiple precision, expansion, division, computational geometry.

*(Résumé : tsvp)*

This work was partially founded by the INRIA Fiable project.

# Algorithme, preuve et performances d'une nouvelle division des expansions de nombres flottants

**Résumé :** Nous présentons dans ce rapport un nouvel algorithme de division sur les expansions de nombres flottants. L'expansion de nombres flottants est un nouveau type de donnée en précision multiple doté d'opérateurs arithmétiques qui utilisent l'unité de calcul en virgule flottante du processeur pour les calculs internes à la place de l'unité de calcul entier. Les recherches sur ce sujet se sont développées à la suite de l'observation que l'unité de calcul en virgule flottante devient une partie de plus en plus puissante des ordinateurs modernes. De nombreux opérateurs arithmétiques simples et quelques opérateurs géométriques utiles ont déjà été définis sur les expansions de nombres flottants. Néanmoins, les travaux antérieurs n'ont présenté qu'une implantation très simple de l'algorithme de division. Nous présentons ici un nouvel algorithme. À cette occasion, nous étendons l'ensemble des opérations géométriques sur les expansions en proposant le calcul du déterminant d'une matrice de taille comprise entre 3 et 10 par la méthode de Bareiss. Nous comparons le temps de calcul avec différents algorithmes de déterminant sur différentes machines avec d'autres logiciels en précision multiples tels que GMP et CADNA et avec un logiciel spécialisé pour la géométrie algorithmique qui utilise l'arithmétique modulaire.

**Mots-clé :** Arithmétique exacte, précision multiple, expansion, division, géométrie algorithmique.

# Introduction

Thanks to both 754 and 854 IEEE standards [17, 18, 3, 7], both the operations on floating point numbers and the behavior of the floating point unit in cases of exceptions are completely specified. Since MFLOPs figures are highly publicized by processor manufacturers, the standard floating point operators are also the most developed and the most powerful arithmetic operators on common processors [9, 8, 16].

Expansions were introduced by Priest in 1991 [13] as a multiple precision tool on floating point numbers based on earlier work by Dekker [6] and Knuth [10]. He proposed algorithms on expansions including the addition, the multiplication and the division. Priest's algorithms were capable to adapt to the machine. They are correct for different working radices and for the different levels of precision achieved on rounding by the floating point unit.

In 1996 and 1997 Shewchuk implemented some enhanced algorithms restricted to the case of an IEEE standard floating point rounding [14, 15]. His library is available on the net<sup>1</sup> with an application to computational geometry. It includes the addition, the subtraction and the scaling of an expansion by a floating point number. One of us proposed in [4, 5] the multiplication of two expansions with a survey on former work on expansions.

The first section of this research report presents definitions and properties of floating point numbers and floating point expansions. We have isolated a new lemma that is used in this paper. We study in section two a new division operator with a look to Priest's prior division algorithm. Our modified algorithm will enable us to implement on-line most significant digit first adaptive computing. The third section proves the exact halting of both algorithms: when the result of the division exactly fits in an expansion, both algorithms find it. The last section, outlines an application of expansions to compute the exact determinant of a small matrix (size 3 to 10) as it may be used in computational geometry. We present three different algorithms and significant running times on two different IEEE machines. This paper ends with concluding remarks and directions for further developments.

## 1 Definitions and properties

### 1.1 Floating point numbers

A **floating point number** is stored in binary as a finite length fraction and a bounded exponent as presented in figure 1. It reads

$$x = (-1)^{\text{sign}} \cdot (1.\text{fraction}) \cdot 2^{\text{exponent}}$$

With this notation, a weight can be associated to each bit of the mantissa. Later on, we call  $\alpha(x)$  the weight of the most significant non-zero bit of  $x$  and  $\omega(x)$  the weight of its least significant non-zero bit. For a normalize number  $x$ ,  $\alpha(x)$  is the exponent of  $x$ . We define the **ulp** function that means "Unit in the Last Place" as the weight of the last bit of the mantissa of  $x$ . For any non zero floating point number, we define  $m(x) = \frac{x}{2^{\alpha(x)}}$ ;  $m(x)$  is the mantissa of  $x$  and  $|m(x)| \in [1, 2 - \text{ulp}]$ .

**Properties** Let  $a$  and  $b$  be two floating point numbers. We know that:

$$\alpha(a) \leq |a| < 2\alpha(a) \tag{1}$$

$$\alpha(a)\alpha(b) \leq \alpha(ab) \leq 2\alpha(a)\alpha(b) \tag{2}$$

$$|b| \cdot \alpha(a) < 2|a| \cdot \alpha(b) \tag{3}$$

**Proof of property (1) :** Since  $|a| = 2^{\alpha(a)}|m(a)|$  and  $|m(a)| \in [1, 2)$  then  $\alpha(a) \leq |a| < 2\alpha(a)$ .  $\square$

**Proof of property (2) :** We expand  $\alpha(ab) = \alpha(\alpha(a)m(a) \cdot \alpha(b)m(b)) = \alpha(a)\alpha(b)\alpha(m(a)m(b))$ . Since  $|m(a)m(b)| \in [1, 4)$  we know that  $\alpha(m(a)m(b)) \in \{1, 2\}$  and we find  $\alpha(a)\alpha(b) \leq \alpha(ab) \leq 2\alpha(a)\alpha(b)$ .  $\square$

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<sup>1</sup>URL: <http://www.cs.cmn.edu/~jrs>.

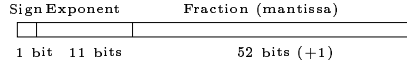


Figure 1: Representation of floating point numbers in IEEE standard double precision

**Proof of property (3) :** Since both  $|m(a)|$  and  $|m(b)|$  are in  $[1, 2)$ ,  $|m(a)| < 2|m(b)|$ . Following the definition of  $|m(a)|$  and  $|m(b)|$ , we have  $|b| \cdot \alpha(a) < 2|a| \cdot \alpha(b)$ .  $\square$

The IEEE standard describes four rounding modes but the rounding to the nearest floating point number is the rounding mode used by default in most computers. The result of any implemented operation, namely the addition, the multiplication, the division and the square root extraction, is the rounded result of the exact mathematical operation. For example, if  $\circ(x)$  is the rounded to the nearest value of  $x$  for any  $x$ , and the machine floating point addition of  $a$  and  $b$  is  $a \oplus b$ , then  $a \oplus b = \circ(a + b)$ .

As proposed by Dekker [6] and Knuth [10], an exact two-sum operator can be constructed from standard floating point operators. The result of the addition is a pair  $(a', b')$  such that  $a' = \circ(a + b)$  and  $a' + b' = a + b$ . An exact multiplication is also available. It computes a pair  $(a', b')$  such that  $a' = \circ(a \cdot b)$  and  $a' + b' = a \cdot b$ . It was proved that  $b'$  always fits in a common floating point number. The exact sum needs 4 floating point additions and 2 floating point subtractions. In particular cases, a fast exact sum only requires 2 floating point additions and one floating point subtraction. These operators are surveyed in [4, 5] with a new improved condition to apply the fast exact sum.

**Definition 1** Let  $\mathbf{A}$  be a set of floating point numbers with its sum  $A = \sum_{a \in \mathbf{A}} a$ . We say that  $\hat{A}$  is a **fair most significant component** of  $A$  if either:

- $\hat{A}$  equals to the rounded to the nearest value of  $A$
- $\hat{A}$  equals to the rounded to the nearest value of  $A_1$  with  $|A - A_1| \leq \text{ulp}(\text{ulp}(\hat{A}))$

**Properties** Let  $\mathbf{A}$  be a set of floating point numbers with its sum  $A = \sum_{a \in \mathbf{A}} a$ , and  $\hat{A}$  a fair most significant component. The following properties are satisfied:

$$|A - \hat{A}| \leq \text{ulp}\left(\frac{1}{2} + \text{ulp}\right) \alpha(\hat{A}) \quad (4)$$

$$\frac{|\hat{A}|}{2} \leq |A| \leq 2|\hat{A}| \quad (5)$$

$$\frac{1}{2}\alpha(\hat{A}) \leq \alpha(A) \quad (6)$$

$$\alpha(A) \leq \alpha(\hat{A}) \quad (7)$$

**Proof of property (4)** Thanks to definition 1, we know that  $\hat{A} = \circ(A_1)$  and  $|A - A_1| \leq \text{ulp}(\text{ulp}(\hat{A}))$ .

$$\begin{aligned}
|A - \hat{A}| &\leq |A - A_1| + |A_1 - \hat{A}| \\
&\leq \text{ulp}(\text{ulp}(\hat{A})) + \frac{1}{2}\text{ulp}(\hat{A}) \\
&\leq \text{ulp}(\alpha(\hat{A}) \cdot \text{ulp}) + \frac{1}{2}\alpha(\hat{A}) \cdot \text{ulp} \\
&\leq \alpha(\hat{A}) \cdot \text{ulp}^2 + \frac{1}{2}\alpha(\hat{A}) \cdot \text{ulp} \\
&\leq \text{ulp}\left(\frac{1}{2} + \text{ulp}\right) \alpha(\hat{A})
\end{aligned}$$

□

Property (5) is a strict corollary of property (4) and its proof is omitted here.

**Proof of property (6)** Thanks to property 4, we know that,

$$\begin{aligned} |A - \hat{A}| &\leq \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) \alpha(\hat{A}) \\ &\leq \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) |\hat{A}| \end{aligned} \tag{8}$$

So there exists  $\epsilon \in [-1, 1]$  such that:

$$A - \hat{A} = \epsilon \cdot \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) \hat{A}$$

Then

$$\begin{aligned} A &= \left( \epsilon \cdot \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) + 1 \right) \hat{A} \\ \alpha(A) &= \alpha \left( \left[ \epsilon \cdot \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) + 1 \right] \hat{A} \right) \\ \alpha(A) &\geq \alpha \left( \epsilon \cdot \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) + 1 \right) \alpha(\hat{A}) \end{aligned}$$

As

$$\epsilon \cdot \text{ulp} \left( \frac{1}{2} + \text{ulp} \right) + 1 \geq \frac{1}{2}$$

Then

$$\alpha(A) \geq \frac{1}{2} \alpha(\hat{A})$$

□

**Proof of property (7)** As we did in the proof of property (6), we can find  $\epsilon$  in  $[-1, 1]$  such that:

$$\begin{aligned} |A - A_1| &\leq \text{ulp}(\text{ulp}(\hat{A})) \leq \text{ulp}^2 \cdot \alpha(\hat{A}) \leq 2\text{ulp}^2 \cdot \alpha(A) \\ A - A_1 &= 2\epsilon \cdot \text{ulp}^2 \cdot \alpha(A) \\ A_1 &= \alpha(A)(m(A) + 2\epsilon \cdot \text{ulp}^2) \end{aligned}$$

It follows that:

$$\begin{aligned} \hat{A} &= \circ(\alpha(A)(m(A) + 2\epsilon \cdot \text{ulp}^2)) \\ &= \alpha(A) \circ (m(A) + 2\epsilon \cdot \text{ulp}^2) \\ |\hat{A}| &\geq \alpha(A) \circ (1 - 2\text{ulp}^2) \\ &\geq \alpha(A) \end{aligned}$$

Since  $\circ(1 - 2\text{ulp}^2) = 1$ . It follows that  $\alpha(\hat{A}) \geq \alpha(A)$ .

□

The six properties presented above and the following lemma are very general results on standard floating point numbers as well as the lemmas presented in [4, 5]. They will probably be applied in the future to other situations. We have started building an adapted *corpus* of sensible results on floating point operations.



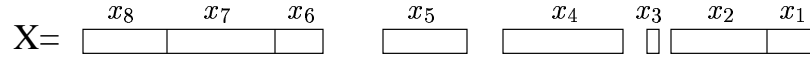


Figure 2: Representation of an expansion

**Lemma 1** *Let  $\mathbf{A}$  be a set of floating point numbers with its sum  $A = \sum_{a \in \mathbf{A}} a$  such that  $\hat{A}$  is a most significant component. The following property is satisfied for any floating point number  $b$  and any set of floating point numbers  $\mathbf{C}$  such that  $C = b.A$  and  $\hat{C}$  is a most significant component of  $C$ .*

$$\alpha(\hat{C}) \leq 2\alpha(b) \cdot \alpha(\hat{A})$$

**Proof :** In the most general case,  $\hat{C} = \circ(C_1)$  with  $|C - C_1| \leq \text{ulp}(\text{ulp}(\hat{C}))$

$$\hat{C} = \circ(C_1) = \circ(bA + e)$$

with

$$\begin{aligned} |e| &\leq \text{ulp}^2 \cdot \alpha(\hat{C}) \\ &\leq 2\text{ulp}^2 \cdot \alpha(C) \\ &\leq 2\text{ulp}^2 \cdot \alpha(bA) \\ &\leq 4\text{ulp}^2 \cdot \alpha(b)\alpha(A) \end{aligned}$$

Let  $e = \epsilon \cdot \text{ulp}^2 \cdot \alpha(b)\alpha(A)$ , with  $\epsilon \in [-4, 4]$ . We know that:

$$\begin{aligned} \hat{C} &= \circ(\alpha(b)\alpha(A)m(A)m(b) + \epsilon \cdot \text{ulp}^2 \cdot \alpha(b)\alpha(A)) \\ &= \alpha(b)\alpha(A) \circ (m(A)m(b) + \epsilon \cdot \text{ulp}^2) \end{aligned}$$

As  $|m(b)| \leq 2 - \text{ulp}$  and  $|m(A)| \leq 2$ ,  $|m(A)m(b) + \epsilon \cdot \text{ulp}^2| \leq 2(2 - \text{ulp}) + 4\text{ulp}^2$  and

$$|\circ(m(A)m(b) + \epsilon \cdot \text{ulp}^2)| \leq 2(2 - \text{ulp})$$

It follows that:

$$\begin{aligned} \alpha(\hat{C}) &\leq \alpha(\alpha(b)\alpha(A) \cdot 2(2 - \text{ulp})) \\ &\leq \alpha(b)\alpha(A) \cdot \alpha(2(2 - \text{ulp})) \\ &\leq 2\alpha(b)\alpha(A) \\ &\leq 2\alpha(b)\alpha(\hat{A}) \end{aligned}$$

□

## 1.2 Expansions

An **expansion** is the representation of a large floating point quantity,  $\mathbf{x}$ , by an  $n$ -tuple of machine floating point numbers  $(x_1, x_2, \dots, x_n)$  called the **components** (see figure 2). The **length** of the expansion is the number of its components.

The components must be sorted by magnitude and two components cannot have significant bits with the same weight as illustrated figure 3. We say that the components are **non-overlapping**. For any value of  $i$ , two non zero components  $x_{i+1}$  and  $x_i$  satisfy  $\omega(x_{i+1}) > \alpha(x_i)$ .

For any mathematic operation,  $\mathbf{x}$  is equal to the exact sum of all its components. The addition and the multiplication on expansions are implemented with exact operations. No information is lost. For example,

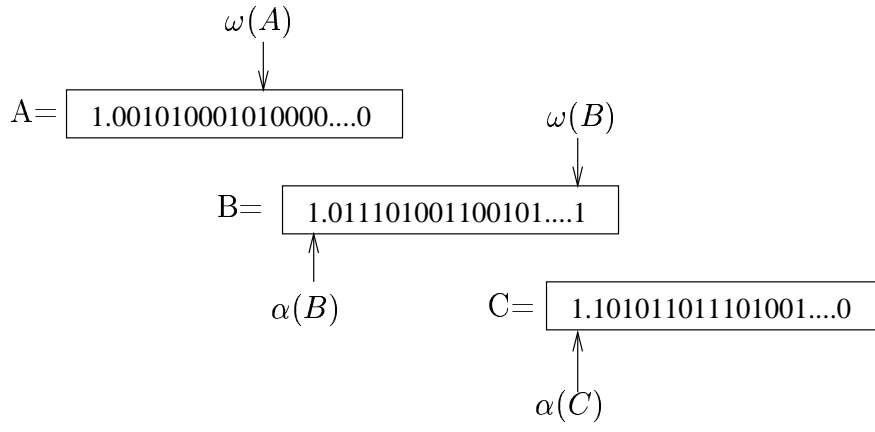


Figure 3: Floating point numbers A and B do not overlap whereas B and C do

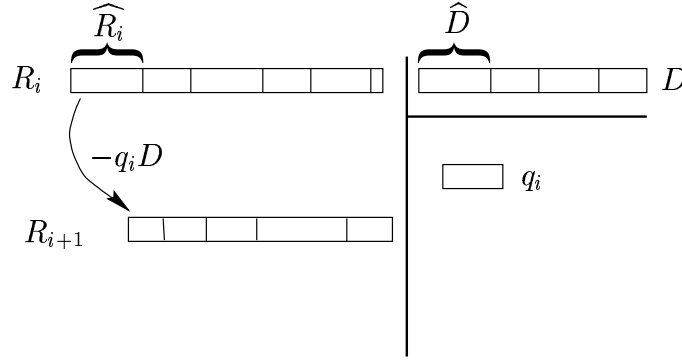


Figure 4: Iteration  $i$  of Priest's algorithm:  $R_{i+1} = R_i - q_i \cdot D$ .

the sum  $2^{200} + 1$  is represented by two components  $2^{200}$  and 1. We do not store all the zero bits between the two components.

Priest proposed a non-restoring algorithm for the division of expansions [12, 11]. It initially stores the value of the dividend in the remainder  $R_0$ . At iteration  $i$ , a new approximate **quotient digit**  $q_i$  is guessed from a fair most significant component  $\widehat{D}$  of the divisor  $D$  and a fair most significant component  $\widehat{R}_i$  of the remainder  $R_i$ :  $q_i = \circ(\widehat{R}_i / \widehat{D})$ . The remainder is replaced by  $R_{i+1} = R_i - q_i \cdot D$  as presented in figure 4.

Since  $R_i$  is at least divided by a constant factor at each iteration, the algorithm is converging. The quotient may be truncated to a given precision to obtain an approximate expansion with a bounded number of components.

$$\frac{R_0}{D} = \sum_{i=0}^{n-1} q_i + \frac{R_n}{D}$$

## 2 Modified algorithm

Our algorithm only estimates the most significant digit  $\widehat{R}_i$  of the remainder. The less significant components of the dividend are stored unchanged with all the components of the terms  $-q_i \cdot D$  that have not been reduced so far. A priority queue extracts the most significant components among this set. These numbers are accumulated until two non zero non overlapping digits  $\widehat{R}_i$  and  $\widetilde{R}_i$  can be estimated. This process guarantees that  $\widehat{R}_i$  is a faithful approximation of  $R_i$ . Figure 5 outlines the relative position of the different quantities summed by our algorithm.

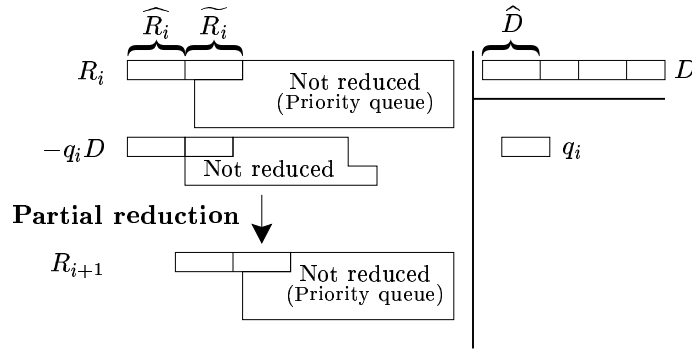


Figure 5: Iteration  $i$  of our modified algorithm: estimate  $\widehat{R}_{i+1}$  and  $\widetilde{R}_{i+1}$ .

## 2.1 Convergence

The new remainder  $R_{i+1}$  is the sum of three terms (see equation 9). The quantity  $(R_i - \widehat{R}_i)$  represents all the digits of  $R_i$  but the most significant one, the second term is a single floating point number and the remaining product is represented as the sum of 2-number expansions sorted by magnitude or as 2 expansions of length  $(n - 1)$  as  $n$  is the length of  $D$ .

$$R_{i+1} = (R_i - \widehat{R}_i) + (\widehat{R}_i - q_i \widehat{D}) - q_i (D - \widehat{D}) \quad (9)$$

Our algorithm is converging since the sequence of  $|R_i|$  is quickly decreasing as presented below.

$$|R_{i+1}| \leq |R_i - \widehat{R}_i| + |\widehat{R}_i - q_i \widehat{D}| + \left| \frac{\widehat{R}_i}{\widehat{D}} (\widehat{D} - D) \right| \quad (10)$$

We present an upper bound of each term of inequation (10).

- At iteration  $i$ , the priority queue contains at most  $(2i + 1)$  expansions since each  $q_i(D - \widehat{D})$  adds two expansions to the queue: one for the upper part of the individual product  $(q_i \times D_j)_H$  and one for the lower part  $(q_i \times D_j)_L$  for  $j = 2..n$ .

$$\begin{aligned} |R_i - \widehat{R}_i| &\leq (2i + 1)ulp(\widehat{R}_i) \\ &\leq (2i + 1)\alpha(\widehat{R}_i)ulp \\ &\leq 2(2i + 1)|R_i|ulp \end{aligned}$$

- $|\widehat{R}_i - q_i \widehat{D}|$  can be reduced from the definition of  $q_i$  using as above the properties (1),(6) plus now property (5).

$$\begin{aligned} |\widehat{R}_i - q_i \widehat{D}| &= \left| \widehat{D} \left( \frac{\widehat{R}_i}{\widehat{D}} - \circ \left( \frac{\widehat{R}_i}{\widehat{D}} \right) \right) \right| \\ &\leq \left| \widehat{D} \right| \frac{1}{2} \cdot ulp \left( \circ \left( \frac{\widehat{R}_i}{\widehat{D}} \right) \right) \\ &\leq \frac{1}{2} \left| \widehat{D} \right| \cdot 2 \left| \frac{\widehat{R}_i}{\widehat{D}} \right| ulp \\ &\leq 2 |R_i| ulp \end{aligned} \quad (11)$$

- The last term is bounded by a function of  $R_i$ .

$$\begin{aligned}
\left| \frac{\widehat{R}_i}{\widehat{D}} (\widehat{D} - D) \right| &\leq \left| \frac{\widehat{R}_i}{\widehat{D}} \right| \cdot ulp(\widehat{D}) \\
&\leq \left| \frac{\widehat{R}_i}{\widehat{D}} \right| \alpha(\widehat{D}) \cdot ulp \\
&\leq \left| \frac{\widehat{R}_i}{\widehat{D}} \right| |\widehat{D}| \cdot ulp \\
&\leq 2 |R_i| \cdot ulp
\end{aligned} \tag{12}$$

Thus, we can write:

$$|R_{i+1}| \leq (4i + 6)ulp \cdot |R_i|$$

Consequently, the algorithm is converging as soon as  $i \leq \frac{ulp^{-1}}{8}$ .

## 2.2 Compared complexity

We consider that evaluating the number of floating point operations (additions, multiplications, divisions and comparisons) is sufficient to compare our algorithm with Priest's one. Let  $n$  be the length of the divisor,  $l_i$  be the length of the remainder at iteration  $i$  and  $m$  be the number of iterations. The length of the quotient is subsequently directly defined as  $m$ .

Both algorithms need  $n$  multiplications and  $3n$  additions to split the components of the divisor and  $m$  multiplications and  $3m$  additions to split the components of the quotient digits as they are produced.

In Priest's algorithm, each iteration is broken down into 4 steps: compute a new digit of the quotient; compute the quantity that should be withdrawn from the remainder; compute the new remainder and finally compress it.

Step	1	2	3	4
Addition		$4nm$	$\sum_i (l_i + 2n) \times 3$	$\sum_i (l_i + 2n) \times 9$
Comparison			$\sum_i (l_i + 2n) \times 1$	
Multiplication		$5nm$		
Division	$m$			

In our algorithm, we can evaluate directly the number of operations from the number of quotient digits produced. Each quotient digit generates a given number of tasks. Steps 1 and 2 are unchanged except that step 1 also includes a test for exact halting (see section 3). Steps 3 and 4 are replaced by the elimination of the terms of the queue: manage the priority queue and consume the topmost digit if time is correct.

Step	1	2	3	4
Addition	$2m$	$4nm$		$m + 2m(n - 1) \times 9$
Comparison	$m$		$2m(n - 1) \times \log m$	$m + 2m(n - 1)$
Multiplication	$m$	$5nm$		
Division	$m$			

To bound the complexity of Priest's algorithm, we have to evaluate  $l_i$ . In the worst case, we can have  $l_i \leq l_{i-1} + 2n$  leading to  $l_i \leq 2in + l_0$ . We can however assume that we have a situation close to the one experienced with multiple precision arithmetic based on integers (later on called integer-like algorithm). In this case, we say that  $l_i \leq m - i + n + \lambda$  where  $\lambda$  is a constant.

Both algorithms require  $5nm + m + n$  multiplications and  $m$  divisions. Our algorithm requires  $m$  additional multiplications to preserve exact halting as presented in the next section. The total number of floating point

additions and comparisons for each algorithm is given below. Managing the priority queue is quite expensive. We have a better asymptotic complexity than Priest's algorithm in general, but Priest's algorithm is faster on simple cases where the enhanced control given by the priority queue is not necessary.

Algorithm	Additions	Comparisons
Priest's	$12nm^2 + 16nm + 3n + 3m$	$2nm + nm(m-1)$
Integer-like	$6m^2 + 40nm + m(12\lambda - 3) + 3n$	$3nm + \lambda m + \frac{m(m-1)}{2}$
Modified	$23mn - 2m + 3n$	$2m(n-1) \log m + 2mn$

### 3 Exact halting

When the quotient may be represented as a floating point expansion, both algorithms find it. They halt after a finite number of steps with the exact quotient. We show that the rounding errors introduced in the approximate guesses of the quotient digits are not sufficient to create an infinite sequence of approximate quotient digits that would converge to the quotient but fail to attain it exactly.

Related questions are entirely new in the literature on floating point operations. Interestingly, we were forced to modify our algorithm to obtain exact halting. This led us to a different proof for Priest's algorithms and for our algorithm.

#### 3.1 Priest's algorithm

We focus our interest on the step  $k$  where the algorithm would finish if it finds the correct floating point quotient digit. The difference between the exact result and the sum of the  $q_i$  we have computed so far, is a floating point number,  $q = R_k/D$ . Let  $q_k$  be the value computed by the algorithm and  $q'_k$  the exact (not rounded) quotient of fair most significant components of  $R_k$  and  $D$ :  $q'_k = \widehat{R}_k/\widehat{D}$ . Here, we assume as Shewchuk conjectured in [15] that the compress algorithm returns a fair most significant component.

$$\begin{aligned}
|q - q'_k| &= \frac{1}{\widehat{D}} |q\widehat{D} - q'_k\widehat{D}| \\
&\leq \frac{1}{\widehat{D}} (|q\widehat{D} - qD| + |R_k - \widehat{R}_k|) \\
&\leq \frac{1}{\widehat{D}} \left( \left| q \cdot ulp\left(\frac{1}{2} + ulp\right) \alpha(\widehat{D}) \right| + \left| ulp\left(\frac{1}{2} + ulp\right) \alpha(\widehat{R}_k) \right| \right) && \text{Using property (4)} \\
&\leq \frac{1}{\widehat{D}} \cdot ulp\left(\frac{1}{2} + ulp\right) (2\alpha(q) \cdot \alpha(\widehat{D}) + \alpha(q) \cdot \alpha(\widehat{D})) && \text{Using lemma 1} \\
&\leq \frac{1}{\widehat{D}} 4ulp\left(\frac{1}{2} + ulp\right) \cdot \alpha(q) \cdot \alpha(\widehat{D}) \\
&\leq \alpha(q) \cdot 4ulp\left(\frac{1}{2} + ulp\right) \\
&< \frac{5}{2} \cdot ulp \cdot \alpha(q)
\end{aligned} \tag{13}$$

$$\tag{14}$$

As shown on figure 6 - where crosses are representable floating point values, the value of  $|q - q_k|$  equals to 0, 1 or 2 ulps. We have 2 different cases:

- $|q - q_k| = 0$ . The algorithm finds the exact result.
- $|q - q_k| = 2^n$ . The exact result will be found at the next step.

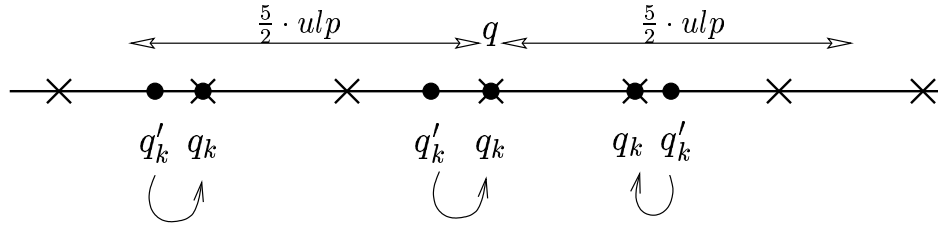


Figure 6: Difference between  $q$  and  $q_k$

### 3.2 Modified division

Since it only estimates the most significant component of the remainder, our algorithm may produce an infinite string of approximated quotient digits. We have inserted a correction in the main loop to avoid this situation.

At each iteration of both algorithms, the quotient digit has to be split into two half words to compute the terms  $-q_i \cdot D$ . In our algorithm, if the quotient digit is very close to its higher half word, we consider that the lower half word is not relevant. For example, an estimated quotient digit of 1.0100000000...0001, 1.0011111111...1100 or 1.0100000000...0110, will be replaced by 1.0100000000...0000.

If the lower part of the digit were correctly estimated, this operation only lengthens the algorithm of one iteration. As expected, this event happens rarely in actual applications.

## 4 Application

As an example, we used expansions to compute the determinant of a small matrix (size 3 to 10). Evaluating the extended sign of a determinant (zero, positive or negative) is a crucial issue in computational geometry. The algorithm has to be fast and error free.

$$\mathcal{A} = \begin{pmatrix} a_{11} & a_{12} & \cdots & a_{1n} \\ a_{21} & a_{22} & \cdots & a_{2n} \\ \vdots & \vdots & \ddots & \vdots \\ a_{n1} & a_{n2} & \cdots & a_{nn} \end{pmatrix}$$

Let  $\mathcal{A}$  be the  $n \times n$  square matrix defined above. Let  $m_{ij}$  be the minor associated to  $a_{ij}$ . To compute the determinant, we first applied the **column wise development** on  $\mathcal{A}$  as follows. We recursively applied the development on the  $m_{i1}$  determinants that need to be computed to finish the computation. Some limited dynamic programming was implemented to reduce the total running time.

$$\begin{aligned} \det(\mathcal{A}) &= a_{11}m_{11} - a_{21}m_{21} + a_{31}m_{31} - \dots + (-1)^{n+1}.a_{n1}m_{n1} \\ &= \sum_{i=1}^n (-1)^{i+1} a_{i1} \times m_{i1} \end{aligned}$$

**Gaussian elimination** uses operations on the lines of the matrix to compute an upper triangular matrix. The product of the terms on the diagonal is directly connected to the determinant of  $\mathcal{A}$ . We used partial pivoting when necessary. Our second algorithm is described below without divisions.

```

tmp=1;
For k = 1 to n-1 do
  For i = k+1 to n do
    For j = k+1 to n do
       $a_{ij} = a_{kk} * a_{ij} - a_{ik} * a_{kj}$ 
     $tmp = tmp / a_{kk}$ 
   $tmp = tmp * a_{kk}$ 
return tmp

```

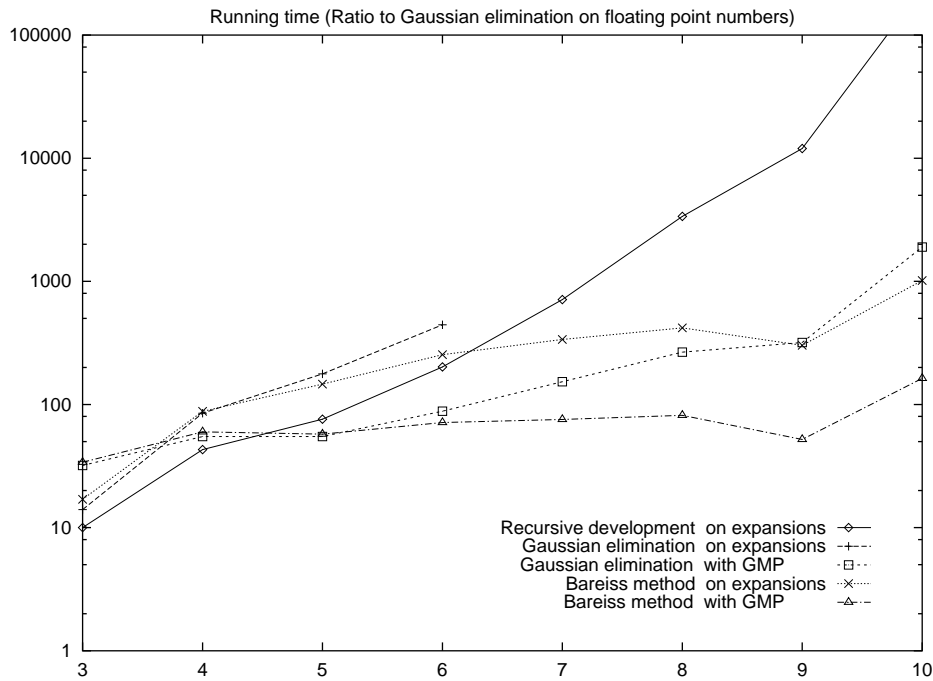


Figure 7: Results on a Cyrix 6x86 with a random matrix.

The magnitude and the length of the intermediate values in our Gaussian elimination grow up very quickly leading to an exponential growth. **Bareiss' method** introduces a division at each step to reduce the size of the intermediate coefficients. The division is exact and the quotient is not approximated. The algorithm becomes:

```

simp = 1;
For k = 1 to n-1 do
  For i = k+1 to n do
    For j = k+1 to n do
       $a_{ij} = (a_{kk} * a_{ij} - a_{ik} * a_{kj}) / simp$ 
     $simp = a_{kk}$ 
  return  $a_{nn}$ 

```

We compared the three different algorithms on our package, on GMP and on regular floating point implementations. The later produced constantly erroneous results as soon as the size of the matrices reached 4. We however used them as a reference. The figures 7 to 9 present a ratio of the running time compared to Gaussian elimination on floating point numbers. This ratio is the price to be paid to obtain the correct result. GMP (GNU Multiple Precision package) is a well-known library that uses assembly code on the integer unit to construct very efficient multiple precision operators.

In our test, we have used two different types of matrices: **random matrices** (fig. 7 and 8) and **non invertible matrices** (fig. 9). The former use 57 bit random integers. The later use ill formed 200 bit integers computed so that the rank of the matrix is  $n - 1$ .

For all the tests, column wise development showed good performances with small matrices. Gaussian elimination was running correctly for medium size matrices. Bareiss' elimination is the best algorithm as soon as the additional cost of the division is amortized by the length of the expansion.

The two different machines involved in the test contain a Cyrix 6x86 for the first one and an Intel Pentium Pro for the second one. The double extended 80 bit format was used to store the components of the expansions.

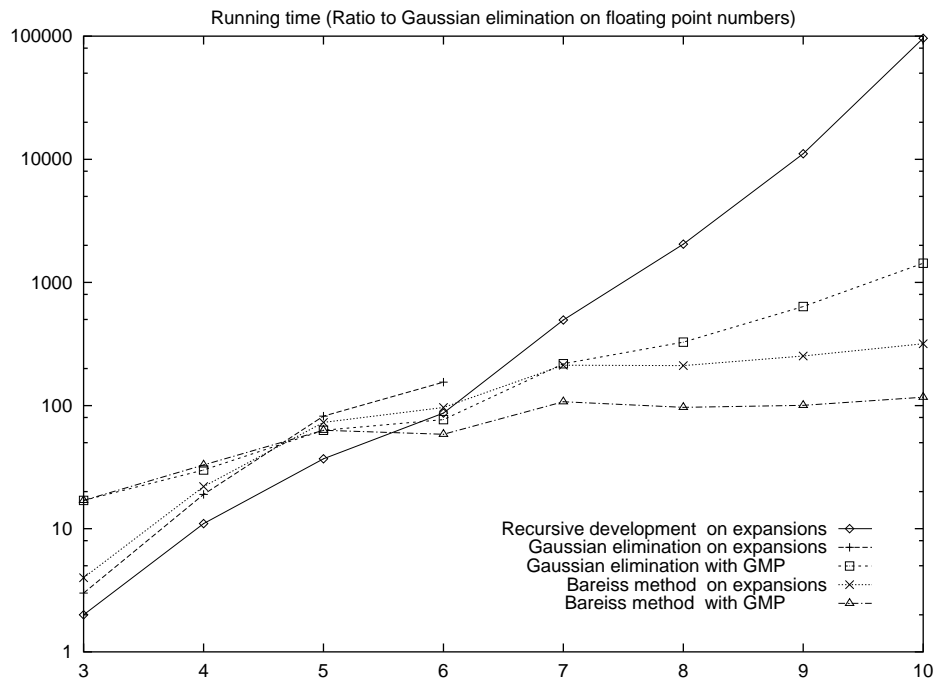


Figure 8: Results on an Intel Pentium Pro with a random matrix.

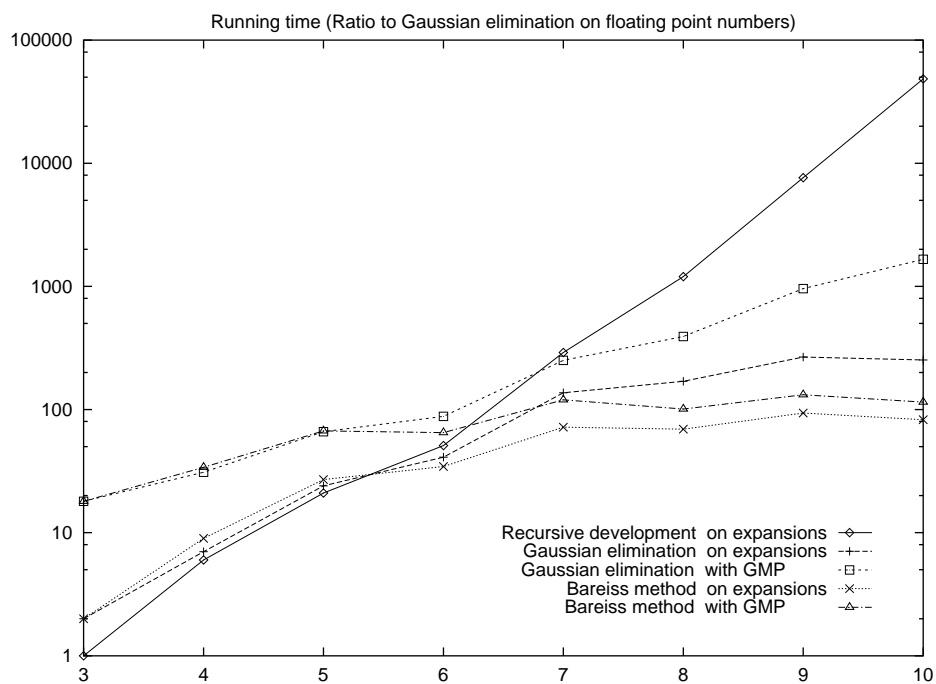


Figure 9: Results on an Intel Pentium Pro with a chosen matrix.



## Conclusion

We now have a complete library of arithmetic operations on expansions and we have tested the performances of expansions on the computation of a small determinant. Results are quite good and with chosen matrices, expansions have better results than widely used libraries like GMP. For the third diagram, the matrices were carefully chosen to give a little advantage to the expansions over GMP. We succeeded since expansions is there constantly the best package.

This situation is representative of a user that does not want to learn about the peculiarities of his code. As it stands, determinants on floating point expansions are a good solution for a package that should run on any situation. However, a hand tuned package still represents a faster solution for a user with a good experience in this field.

Results on the 6x86 are not very good for expansions compared to GMP. The Cyrix 6x86 is known to have a weak floating point unit compared to its integer unit. The second diagram shows a visible improvement with exactly the same tests and exactly the same program. The expansion library uses the floating point unit whereas GMP uses the integer unit. As long as the floating point unit continues to speed up in comparison to the integer unit, expansion will show impressive results compared to integer libraries for this kind of applications.

Priest's algorithm needs all the components of the divisor and the dividend before it begins computation. Our algorithm only requires a few of their most significant components before it produces the most significant digit of the quotient. We say that our division may work on-line with the most significant digits of its operands first. Yet, the run time system able to take advantage of this property is still to be created. One of us presents in [1, 2] a prototype of such system. Odds remains that the user will be provided individual basic tools rather than a transparent system.

The addition and the multiplication operators implemented in [13, 14, 15, 5] operate least significant digits first. We will in near future transform all these operators to operate on-line. This is the natural step to obtain an adaptive library.

## References

- [1] David Berthelot and Marc Daumas. A library for real numbers using Cauchy's embedded interval sequences. In *International Conference on Interval Methods and Computer Aided Proofs in Science and Engineering*, pages 23–24, Würzburg, Germany, 1996.
- [2] David Berthelot and Marc Daumas. Computing on sequences of embedded intervals. *Reliable Computing*, 3(3):219–227, 1997.
- [3] William J. Cody, Richard Karpinski, et al. A proposed radix and word-length independent standard for floating point arithmetic. *IEEE Micro*, 4(4):86–100, 1984.
- [4] Marc Daumas. Multiplications of floating point expansions. Research report 98-39, Laboratoire de l'Informatique du Parallélisme, Lyon, France, 1998.
- [5] Marc Daumas. Multiplications of floating point expansions. In *Proceedings of the 14th Symposium on Computer Arithmetic*, Adelaide, Australia, 1999.
- [6] T. J. Dekker. A floating point technique for extending the available precision. *Numerische Mathematik*, 18(3):224–242, 1971.
- [7] David Goldberg. What every computer scientist should know about floating point arithmetic. *ACM Computing Surveys*, 23(1):5–47, 1991.
- [8] David Goldberg. *Computer Architecture: A Quantitative Approach*, chapter Computer Arithmetic, pages A1–A77. Morgan Kaufmann, 1996.
- [9] John L. Hennessy and David A. Patterson. *Computer architecture: A quantitative approach*. Morgan Kaufmann, 1996.
- [10] Donald E. Knuth. *The Art of Computer Programming: Seminumerical Algorithms*. Addison-Wesley, 1981.
- [11] Israel Koren. *Computer Arithmetic Algorithms*. Prentice Hall, 1993.
- [12] Jean-Michel Muller. *Arithmétique des Ordinateurs*. Masson, 1989.

- [13] Douglas M. Priest. Algorithms for arbitrary precision floating point arithmetic. In Peter Kornerup and David Matula, editors, *Proceedings of the 10th Symposium on Computer Arithmetic*, pages 132–144, Grenoble, France, 1991. IEEE Computer Society Press.
- [14] Jonathan R. Shewchuk. Robust adaptative floating point geometric predicates. In *Proceedings of the 12th Annual ACM Symposium on Computational Geometry*, pages 141–150, Philadelphia, Pennsylvania, 1996.
- [15] Jonathan R. Shewchuk. Adaptive precision floating-point arithmetic and fast robust geometric predicates. In *Discrete and Computational Geometry*, volume 18, pages 305–363, 1997.
- [16] Bruce Shriver and B. Smith. *The anatomy of a high-performance microprocessors — A systems perspective*. IEEE Computer Society Press, 1998.
- [17] David Stevenson et al. A proposed standard for binary floating point arithmetic. *IEEE Computer*, 14(3):51–62, 1981.
- [18] David Stevenson et al. An american national standard: IEEE standard for binary floating point arithmetic. *ACM SIGPLAN Notices*, 22(2):9–25, 1987.



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Unité de recherche INRIA Lorraine, Technopôle de Nancy-Brabois, Campus scientifique,  
615 rue du Jardin Botanique, BP 101, 54600 VILLERS LÈS NANCY  
Unité de recherche INRIA Rennes, Irisa, Campus universitaire de Beaulieu, 35042 RENNES Cedex  
Unité de recherche INRIA Rhône-Alpes, 655, avenue de l'Europe, 38330 MONTBONNOT ST MARTIN  
Unité de recherche INRIA Rocquencourt, Domaine de Voluceau, Rocquencourt, BP 105, 78153 LE CHESNAY Cedex  
Unité de recherche INRIA Sophia-Antipolis, 2004 route des Lucioles, BP 93, 06902 SOPHIA-ANTIPOLIS Cedex

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Éditeur  
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